Work all problems. Closed book, closed notes; You may use the supplied reference material. Powers of 2: $1K = 2^{10}$, $1M = 2^{20}$, $2^9 = 512$, $2^8 = 256$, $2^7 = 128$, $2^6 = 64$, $2^5 = 32$, $2^4 = 16$

 10 pts. Assume a 80486 processor (32-bit data bus). Mark the value of the byte enable TRUE (asserted) or FALSE (negated) for each of the following memory read operation the lower 8 data lines (D7-D0), while BE3 is for the upper 8 data lines (D31-D24). 			ne byte enable ead operations. 31-D24).	signals as BE0 is for		
	a. mov	ax, [7FFFC]	BE3 F	BE2 F	BE1 T	BE0 T
	b. mov	al, [CFF06]	F	Т	F	F
	c. mov	ax, [03A49]	F	Т	Т	F

2. (10 pts) Assume a 80486 processor (32-bit data bus). Mark the following accesses as ALIGNED or MISALIGNED.

a.	mov	ax, [0ADCB]	ALIGNED	MISALIGNED
b.	mov	ax, [0ADC1]	ALIGNED	MISALIGNED
b.	mov	eax, [0ADCC]	ALIGNED	MISALIGNED

3. (10 pts) Assume a Pentium processor with a 64 bit wide data bus.

a. How many CLOCK cycles would it take to transfer 128 bytes using normal read cycles? 8 bytes transferred per read cycle, 128/8 = 16 read cycles, 2 clocks per read cycle, so 32 clocks.

b. How many CLOCK cycles would it take to transfer 128 bytes using burst read cycles? 32 bytes transferred per burst cycle, 128/32 = 4 burst cycles, 5 clocks per burst cycle, so 20 clocks

c. How many clock cycles would it take to transfer 128 bytes using pipelined burst read cycles? 32 bytes transferred per burst cycle, 128/32 = 4 burst cycles, 4 clocks per pipelined burst cycle, but first cycle must be non-pipelined burst cycle, so 5 + 3(4) = 17 clocks.



 At 'point A', write an instruction that will read the value of parameter A into register AX. This instruction cannot change the stack (i.e, a 'POP' is incorrect answer). You must use BP as your index register. It will help if you draw a picture of the stack. BE CAREFUL -SUBA is a 'far' procedure call from MAIN!!!

move ax, [bp+16]

b. Write a TWO INSTRUCTION SEQUENCE at the end of 'SUBA' that will clean up the stack frame, return to the main program, and clean up the stack of the passed parameters **A** and **B**.

As corrected in class, this actually takes more than 2 instructions:

- leave pop di pop si pop bx ret 6
- 5. (6 pts) For a memory chip with control lines **CS**, **OE**, and **W**, what control lines must be asserted during:
 - a. a write operation? Both CS and W must be asserted
 - b. a read operation? Both CS and OE must be asserted.

6. (14 pts) Fill in the blanks using one of the following terms. You can also use a number to fill in the blank like 10, 2, 7, etc:

DRAM RDRAM SRAM SSRAM 'Access time' 'Cycle Time' 'Chip Select' 'non-volatile' 'volatile' ' output enable' 'capacitor'

a. This memory technology has a 6 transistor cell: _____{SRAM,SSRAM}_____

b. This memory technology has an internal counter to support burst mode: __{SRAM,RDRAM}__

c. This memory technology has access time = cycle time: {SRAM,SSRAM}

d. This memory technology uses limited swing signaling technology to achieve high bandwidth: _____RDRAM______

e. Time from when address is valid to time when data is valid: _____ACCESS TIME_____

f. # of address pins for a 1M x 1 DRAM: ____10____

g. This memory technology has memory cells which need periodic refreshing: _{DRAM,RDRAM}_____

7. (10 pts) For the address decoder below:

a. Give the range of addresses that output Y6 is valid for (Use HEX addresses)

b. How many TOTAL bytes is this address decoder valid for? (give the answer in Kbytes, or Mbytes)

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	A14 10	Y0
A 19 A 18 A 17 A 16 A 15 A 14 A 13 A 0	$\begin{array}{c c} A15 & & I1 \\ A16 & & I2 \end{array}$	Y1 🖯
$\begin{array}{cccccccccccccccccccccccccccccccccccc$	EN	Y20
	A17	_{Y3} O
A16-A0 = 17 address lines, 2^{17} = 128 Kbytes		Y4 🔿 🗕
	A19	Y5 🔿 ———
		Y6 0
		_{Y7} O

3-to-8 Decoder I2 is MSB, I0 is LSB

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8. (10 pts) Write a subroutine that will copy the NULL terminated string pointed to by register SI to the location pointed to by DI. During the copy operation, ONLY copy the letters 'A'-'Z', and 'a'-'z' – ignore any ASCII codes outside of these ranges. You must copy the NULL terminator as well. On entry to the subroutine, SI points to the string to be copied, and DI points to the destination address.

scopy	proc	near	
	mov	al,[si]	;get a byte
	стр	al,0	;check if at end
	je	exit	;exit if at end
	cmp	al, 'A '	
	jl	skip	; if lower than 'A', skip
	стр	al, 'Z'	
	jle	docopy	; if between 'A'-'Z', copy
	стр	al, 'z '	
	ја	skip	;if higher than 'z' skip
	стр	al, 'a'	
	jb	skip	;if lower than 'a', skip
docopy:	mov	[di],al	;copy it
	inc	di	; increment destination pointer
skip:	inc	si	;increment source pointer
	jmp	scopy	;do it again
exit:	mov	[di],al	;copy null
	ret		
scopy	endp		

9. (10 pts) Write a subroutine that will write the value in AL to the screen in ASCII binary format (i.e., if AL = A3h, then "10100011" would appear on the screen. You can only use the DOS INT 21h single character output function (AH =02, DL has character to output).

pbin	proc	near	
	mov	cx,8	;need to print 8 digits
lp1:	shl	al, 1	shift MSB into carry flag;
	jc	do one	; if $C=1$, then print a '1'
	mov	dl, 30h	;print a '0'
	jmp	print	- , ,
do one:	mov	dl, 31h	;get a '1'
print:	mov	ah,02	; print digit in dl
	int	21h	
	loop	lp I	;loop until 8 digits printed
	ret		

10. (10 pts) Write a subroutine called 'memdump' that will print 16 bytes starting at DS:SI to the screen in the format used by the "DEBUG" program as shown below:

```
C:\users>debug
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0AC3:0100 32 DB 86 1C E8 39 EB 3B D6 73 1B 56 51 8B CE 8B 2....9.;.s.VQ...

Note that the first thing printed is the hex value of DS:SI, followed by hex values of the 16 bytes starting at [DS:SI]. After that, the ASCII representation of the 16 bytes is printed. If the ASCII representation is a non-printable character (ASCII value less than 12H), then a '.' (period) is printed instead.

You may use the DOS single character output function (INT 21H, AH =02, DL has character to output) or any of the Irvine link library subroutines.

As explained in class, assume the existence of writeint_byte function in the Irvine link library than functions the same as writeint, except that it writes an 8 bit value contained in AL.

Dprint	proc	near		
-	Mov	ax,ds		
	Mov	bx,16		
	Call	writeint	print segment value	
	Mov	dl, ': '		
	Mov	ah,02		
	Int	21h	;print ':'	
	Mov	ax,si		
	Mov	bx,16		
	Call	writeint	;print offset value	
	Mov	cx,16	;loop 16 times	
	Push	si	;save SI for later	
Lp1:	mov	dl, 20h	;space char	
	Mov	ah,02		
	Int	21h	;print space character	
	mov	al,[si]		
	call	writeint_byte	;write a byte out	
	inc	si	;point at next byte	
	loop	lp I	;do 16 times	
		11 201		
	mov	al,20n		
	mov	an,02		
1	ini	21N;	;print a space	
;;loop again, print ascii value				
	pop	st	point back at start	
1	mov	CX, 10	;100p 16 times	
<i>lp2</i> :	mov	al,[Sl] dl 12k	, chock if mintable ASCII	
	cmp	$a_{l,12n}$; check if printable ASCII	
	jae	ao_prini	ij yes, inen print it	
do nui	mov	<i>al</i> , .	;gel períod	
uo_prini:				
	int	un,02 21h	print the character	
	ini	21R ln 2	print the character	
	ioop	ip_2	,100p 10 11mes	
	rei			