



Spice Testbench

- I will provide only a single phase clock as in the original testbench
- You will be responsible for generating the two phase clock with or without overlap.
- How you split up your ALU logic between the two phases is up to you.
- You can only use Domino logic.
- Note that you must have at least one domino gate in-between stages, you might need more gates to solve race-through conditions.